

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jin-Kyoung Jung, *et al.*

Examiner: Almo, Khareem E.

Serial No.: 10/799,783

Group Art Unit: 2816

Filing Date: March 12, 2004

Confirmation No.: 8323

Title: INTERNAL VOLTAGE GENERATING CIRCUIT FOR SEMICONDUCTOR  
DEVICE

Customer No.: 29344

Attorney Docket No.: SAM-0529

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CERTIFICATE OF ELECTRONIC SUBMISSION

I hereby certify that this correspondence and any correspondence referred to as being attached or enclosed is being sent by electronic submission to the United States Patent and Trademark Office on the date indicated next to my name.

3/24/08  
Date

Brian Driscoll  
Brian Driscoll

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**Via Electronic Submission**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT G**

Sir:

The following is in response to the Office Action dated December 26, 2007, and is filed within the shortened statutory period of three months. Please amend the application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 15 of this paper.